SHEET 1 OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO.		SERIAL NO. 10 61861		
/ A CONTROL OF A C			240324US-2 TTC DIV					
LICTOR	o e ce	DENCES CITED BY AD	IDÌ ICANT	APPLICANT				
LIST OF REFERENCES CITED BY APPLICANT				Tohru OZAKI, et al.				
				FILING DATE		GROUP 2815		
				HEREWITH		Unassigned 08/3		
	,	·	,	U.S. PATENT DOCUMENTS				
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
Y 344	AA	5,281,555	01/1994	сно	438	425		
980	AB	5,361,234	11/1994	IWASA	365	210		
939	AC	5,410,161	04/1995	NARITA	257	41		
3322	AD	5,689,126	11/1997	TAKAISHI	257	306		
	AE		[1			
	AF							
	AG				1			
	АН							
	AJ							
	AJ							
	AK		 		1	<u> </u>		
	AL							
	AM		<u> </u>					
	AN		 		+			
		DOCUMENT NUMBER	DATE	COUNTRY		TRANSLATION YES NO /		
724	AO	4-05-129552	05-1993	JAPAN				
180	AP	4-08-181290	07-1996	JAPAN				
W.	AQ	4-11-354727	12-1999	JAPAN				
	AR							
	A\$							
	AT							
	AU							
•	AV							
 		OTHER RE	FERENCES (including Author, Title, Date, Pertiner	nt Pages. e	(c.)	- · · · · · · · · · · · · · · · · · · ·	
9X4	AW	Takashima et al., A Su	b-40ns Rando	m-Access Chain FRAM Architecture wit ference, pp. 102, 103, 150		•	e Drive, 1999, IEEE,	
989	'AX	Takeshima et al., Gain Cell Block Architecture for Gigabit-Scale Chain Ferroelctric RAM, June 1999, Symposium on VL Circuits Digest of Technical Papers, pp. 103-04						
	AY							
	ΑZ			☐ Addi		litional References sheet(s) attached		
		1 Sekens						
xaminer	. }			citation is in conformance with MPEP 6	Date Cor	nsidered	aliloy	

I:MTTY/DAB/24'S/240324US/PTO 1449.00C